

IN THE SPECIFICATION

Please replace the two paragraphs starting on page 11, line 6 with the following two paragraphs:

Additionally, the example illustrated in FIG. 3 may provide a number of decoder control circuits 151a-151n. The decoder control circuits 151a-151n may be optional circuits that may be used to control the particular navigation software elements 150a-150n. Additionally, a decoder 153 may be implemented that may control each of the navigation software elements 150a-150n. ~~In one example, the decoder 114a may be connected to the decoder 114 through a universal serial bus or other appropriate interconnection 117.~~

Referring to FIG. 4, an example of a multifunction remote decoder 114 is shown. The remote decoder 114 generally has an input 112 that may receive one of the number of sources 110a-110n. A number of individual decoding elements 200a-200n may be provided to decode the particular source. The signal received at the input 112 may be presented to inputs of the various decoding elements 200a-200n. The outputs of the decoder elements 200a-200n may be presented to an output 210 and an output 212. A multiplexer (not shown) may be implemented to switch between the various outputs of the individual decoder elements 200a-200n. In one example, the

decoder elements 200a-200n may be implemented as a single integrated circuit. However, to provide flexibility for future standards, the individual decoder elements 200a-200n may be implemented as one or more integrated circuits. For example, if the remote decoder 114 is implemented to decode three current video/audio standards, a single chip implementing three individual decoders 200a-200n may be used. However, to implement a fourth standard, a second integrated circuit 114a may be implemented as a new decoder and may be configured to add to the existing decoders. In one example, the decoder 114a may be connected to the decoder 114 through a universal serial bus or other appropriate interconnection 117.